



PLICANT:

Aravind Sitaraman et al.

SERIAL NO.:

10/698,807

**CONFIRMATION NO.: 2447** 

FILING DATE:

October 31, 2003

TITLE:

METHOD AND APPARATUS FOR CREATING BUNDLE OF

SOFT PERMANENT VIRTUAL CIRCUITS

**EXAMINER:** 

Patel, Chandrahas B.

TELEPHONE: (571) 270-1211

**ART UNIT:** 

2616

FAX: (571) 273-8300

## **CERTIFICATE OF MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450, on the date printed below:

Date: September 11, 2007

Mail Stop: Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## AMENDMENT AND RESPONSE TO OFFICE ACTION

Dear Sir:

This paper is responsive to the Office Action mailed June 11, 2007. Please amend the above-identified application as follows:

Listing of Claims begins on page 2.

Amendments to the Drawings begin on page 18 of this paper and include attached replacement sheets.

Remarks begin on page 19 of this paper.

An Appendix including the following document is attached following page 34 of this paper:

U.S. Patent Office document entitled, "Examination Guidelines For Claims Reciting A 'Means or Step Plus Function' Limitation in Accordance With 35 U.S.C. § 112, 6<sup>th</sup> Paragraph."